

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of Invention

STRUCTURE AND METHOD FOR LOCAL RESISTOR
ELEMENT IN INTEGRATED CIRCUIT TECHNOLOGY

Application Number :

Confirmation Number:

First Named Applicant: Jason Gill

Attorney Docket Number: BUR920030118US1

Art Unit:

Examiner:

Search string: (5232865 or 5330930 or 5665629 or 5930638 or 6117792 or 20030116820 or 20020003311).pn

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
	1	5232865	1993-08-03	Manning et al.			
	2	5330930	1994-07-19	Chi			
	3	5665629	1997-09-09	Chen et al.			
	4	5930638	1999-07-27	Reedy et al.			
	5	6117792	2000-09-12	Kitagawa			

US Published Applications

Note: Applicant is not required to submit a paper copy of cited US Published Applications

init	Cite.No.	Pub. No.	Date	Applicant	Kind	Class	Subclass
	1	20030116820	2003-06-26	Daubenspeck et al.			
	2	20020003311	2002-01-10	Uematsu			

Signature

Examiner Name	Date